Name: Dr. G. Hemalatha

Designation: Professor & HOD

D.O.B: 01-06-1976

D.O.J: 29-07-1998

Years of Experience: 22 Years

Andhra Pradesh.

Mobile: 9440004404

E-mail: latha.gumireddy@gmail.com, latha.g@ksrmce.ac.in

Experience Summary:

- Working as Professor in the department of Electronics and Communication Engineering in KSRM College of Engineering, Kadapa, (A.P.) from Oct 2017.
- Worked as Associate Professor in the department of Electronics and Communication Engineering in KSRM College of Engineering, Kadapa, (A.P.) from July 2005 to Sep 2017.
- Worked as Assistant Professor in the department of Electronics and Communication Engineering, KSRMCE, Kadapa from 29-07-1998 to 04. 07. 2005.
- Worked as Teaching Assistant in G. Pulla Reddy Engineering College, Kurnool, from July 1997 to May1998.

Academic Qualifications

- Completed SSC from **St. Joseph High school, Kurnool** in the year **1991**.
- Completed Intermediate from **St. Joseph Junior College for Girls, Kurnool** in the year 1993.
- Awarded B. Tech. degree with First class in **ECE** from **SV University** in the year 1997.
- Awarded M. Tech. degree with distinction in **Electronic Instrumentation and Control Systems** from **SV University** in the year 2004.

- ❖ Awarded doctorate from **SV University** in October 2016 for the title "Efficient extraction of visual evoked potentials from noisy back ground EEG".

Project works guided

- ❖ B. Tech Projects:- **30**
- ❖ M. Tech Projects:- **06**

Subjects Taught under UG & PG Level

UG Level:

- Computer Organization
- Electronic Devices and Circuits
- Analog Circuits
- Electro Magnetic fields
- Electro Magnetic Waves and Transmission Lines
- Pulse and Digital Circuits
- Signals and Systems
- Probability Theory and Stochastic Process
- Antenna and Wave Propagation
- Integrated circuits and applications
- Digital Signal Processing
- Micro Wave Engineering
- Digital Communications
- Electronic Instrumentation and Measurements
- Satellite Communication
- Cellular and Mobile Communication

PG Level:

- Digital Communications
- Coding Theory techniques

Conferences Attended

International

1. G. Hemalatha, Dr. B. Anuradha, "Enhancement of visual Evoked Potentials", International Conference on Electrical, Electronics and Optimization Techniques (ICEEOT) on 04-03-2016 at Chennai.

Journals Published

International

[1] V. Adinarayana Reddy, P. Chandra Sekhar Reddy, G. Hemalatha, B. Anuradha and T. Jayachandra Prasad, "Detection & Removal of Non-responsive Channels and Trials of Evoked Potentials using Median test" *AIRCC, Signal & Image Processing : An International Journal (SIPIJ), ISSN : 0976 - 710X (Online) ; 2229 - 3922 (print),* vol. 2, no. 4, pp. 37-46, December 2011.

[2] V. Adinarayana Reddy, P. Chandra Sekhar Reddy, G. Hemalatha, T. Jaya Chandra Prasad, "Removal of Artifacts in Multi-channel Visual Evoked Potentials", *International Journal of Modern Engineering Research (IJMER) www.ijmer.com* Vol.1, Issue.2, pp-413-417. Nov. 2011.

[3] G. Hemalatha ,V. Adinarayana Reddy and B. Anuradha, "Processing of evoked potentials by using wavelet transforms," *International Journal of Computer Engineering & Technology ISSN 0976 – 6367(Print) ISSN 0976 – 6375(Online) Volume 4, Issue 6,* pp. 403-413, November – December, 2013.

[4] G. Hemalatha ,V. Adinarayana Reddy and B. Anuradha " Efficient extraction of evoked potentials from noisy background EEG," *International Journal of Electronics & Communication Engg. and Technology (IJECET), ISSN: 0976-6464 (print), : 0976-6472 (online),* vol. 4, issue 1, pp. 216-229, January – February, 2013

[5] G. Hemalatha ,V. Adinarayana Reddy and B. Anuradha, "Processing of visual evoked potentials using correlation", International Journal of Electronic and Communication Research, ISSN 2231-1246 Volume 4, Number 1 (2013), pp. 9-18, Research India Publications, http://www.ripublication.com/ijecr.htm.

[6] C. Niloufer fathima and G. Hemalatha, "Performance Evaluation of MIMO-OFDM Systems under Various Channels", International Journal of Electronics & Communication (IIJEC). A Publisher for Research Motivation,Volume 2, Issue 8, August 2014, ISSN 2321-5984.

[7] G. Hemalatha, Dr.B. Anuradha, V. Adinarayana Reddy, "Processing of Visual Evoked Potentials," International Journal of Applied Engineering Research(IJAER),ISSN 1087—1090, Volume 10, Number 14 (2015) pp 34736-34742,© Research India Publications (scopus indexed).

[8] G. Hemalatha, Dr.B. Anuradha, V. Adinarayana Reddy, "Processing of visual Evoked Potentials using Mode Deviation," International Journal of Engineering Trends and Technology – IJETT,Volume 24, Number 3- June 2015,ISSN:2231-5381.

[9] Y. Jahnavi Lakshmi Ramya, G. Hemalatha," An Efficient DFT Based Channel Estimation For OFDM Systems That Maintains Complexity-Performance Tradeoffs And With Leakage Suppression", Journal Research In Electriacal And Electronics (JREECS),Nov-2015.ISSN NO. 4142-3453.

[10] G. Hemalatha, Dr. B. Anuradha, "Detection of Artifacts in Visual Evoked Potentials" International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering (IJIREEICE), ISSN 2324-2004 Vol. 4, Issue 3, March 2016 .

[11] G. Hemalatha, Dr. B. Anuradha, "Enhancement of visual Evoked Potentials", International Conference on Electrical, Electronics and Optimization Techniques (ICEEOT) on 04-03-2016 and will be published in IEEE Xplore Digital Library with IEEE catalog 978-1-4673-9939-5/16/$31.00 ©2016 IEEE.

[12] N. Charan Kumar Reddy, G. Hemalatha, "Enhanced Decimal Matrix Code for Detection and Correction of Multipleb Cell Upsets in SRAM", International Journal Of Advanced Technology And Innovative Research (IJATIR), ISSN 2348–2370 Vol.08, Issue.18, October-2016, Pages:3589-3593.

[13] U. Swamy kumar" and smt. G. Hemalatha, Salient Object Detection in Videos Based on SPATIO-Temporal Saliency Maps and Colour Features",International Journal and Magazine of Engineering, Techn0logy, Management and research (IJMETMR), ISSN: 2348-4845, Volume No. 3, Issue-10, October 2016.

[14] M. Nikitha, and Dr. G. Hema Latha, "A Normal I/O Order Radix-2 FFT Architecture to Process Two Data Streams", International Journal of Scientific Engineering and Technology Research (IJSETR), ISSN: 2319-8885

Vol.06, Issue.27, August-2017, Pages: 5310-5313.

Workshops Attended

- Attended one week work shop on Virtual Instrumentation at MIT, Chennai in the year 2003.
- Attended three days work shop on Signal and Image processing at SVUCE, SV University, Tirupathi, in Feb 2016.
- Attended two day work shop on VHDL Programming on 7,8-05-2017 at KSRMCE, Kadapa, AP .
- Trained by APSCHE British Council from 10-07-2017 to 14-07-2017 and 24-07-2017 to 28-07-2017
- Attended one day work shop on Effective teaching Techniques using MATLAB and SIMULINK on 07-10-2017 at KSRMCE, Kadapa, AP .
- Attended one week workshop on Awareness on criteria of NAAC from 22-12-2017 to 27-12-2017 at KSRMCE, Kadapa.
- Attended one week workshop on Advanced Digital Signal Processing and its Applications from 20-11-2017 to 26-11-2017 at KSRMCE, Kadapa.

Memberships

ISTE Life Membership LM45495

FIE

Administration Service

- Actively participated and worked for various committees like NBA, NAAC AICTE, University Affiliations and Autonomous.
- Worked as class teacher in each Academic year.
- Lab in charge for DSP Lab
- Board of Studies Member of KSRMCE.
- Member of Academic council
- IQAC Coordinator